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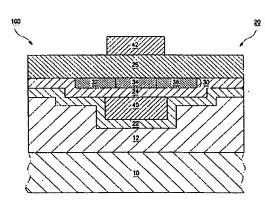
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(54) Title: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING SAME



(57) Abstract: To refine a semiconductor device (100), in particular a S[ilicon]O[n]I[nsulator] device, comprising: at least one isolating layer (10) made of a dielectric material; at least one silicon substrate (20) arranged on said isolating layer (10); at least one component (30) integrated in the silicon substrate (20), which component has at least one slightly doped zone (34); as well as at least a first, in particular planar, metallization region (40) arranged between the isolating layer (10) and the component (30), in particular between the isolating layer (10) and the slightly doped zone (34) of the component (30), as well as a method of manufacturing at least one semiconductor device (100) in such a manner that trouble-free operation also of slightly doped components (30), such as pnp transistors, is guaranteed in a SOI process transferred onto the insulator, it is proposed that at least a second, in particular planar, metallization region (42) is arranged on the side of the silicon substrate (20) facing away from the isolating layer (10), in the area of the component (30), particularly in the area of the slightly doped zone (34) of the component (30).



70 03/103041

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Patent document cited in search report		Publication date		Patent family member(s)	Publication date
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JP 07038005	Α	07-02-1995	NONE		
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H01L29/735

According to International Patent Classification (IPC) or to both national classification and IPC

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Minimum documentation searched (classification system followed by classification symbols) IPC  $\,7\,$  H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

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C. DOCUM	ENTS CONSIDERED TO BE RELEVANT				
Category •	Citation of document, with indication, where appropriate, of the	ne relevant passages	Relevant to claim No.		
X	US 6 331 473 B1 (HIRABAYASHI Y 18 December 2001 (2001-12-18) abstract; claims; figure 13	UKIYA)	1,2,4-10		
X	PATENT ABSTRACTS OF JAPAN vol. 1995, no. 05, 30 June 1995 (1995-06-30) -& JP 07 038005 A (SONY CORP), 7 February 1995 (1995-02-07) abstract; figures		1-5,7,8		
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X Furt	her documents are listed in the continuation of box C.	Palent family members are listed	in annex.		
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	ation) DOCUMENTS CONSIDERED TO BE RELEVANT	Delevent to stall the
Category •	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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JP 58012353 3	Α		NONE		

### 国際關查報告

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A. 発明の属する分野の分類 (国際特許分類 (IPC)) Int. Cl <sup>7</sup> H04B7/26						
B. 調査を行った分野 調査を行った分野 調査を行った最小限資料(国際特許分類(IPC)) Int. Cl <sup>1</sup> H04B7/24…7/26 H04Q7/00-7/38						
最小限資料以外の資料で調査を行った分野に含まれるもの 日本国実用新案公報 1922-1996年 日本国公開実用新案公報 1971-2003年 日本国登録実用新案公報 1994-2003年 日本国実用新案登録公報 1996-2003年	·					
国際調査で使用した電子データベース(データベースの名称、調査に使用した用語)						
C. 関連すると認められる文献						
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カテゴリー* 引用文献名 及び一部の箇所が関連する	ときは、その関連する箇所の表示	請求の範囲の番号				
EX JP 2003-264873 A EA 2003.09.19 第6欄第9行-第12欄第43行	•	1-4, 8 5-7				
X   C欄の続きにも文献が列挙されている。	□ パテントファミリーに関する別	紙を容照。				
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